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(71) Applicant (for all designated States except US):  
**TRUSTEES OF BOSTON UNIVERSITY [US/US];**  
One Sherborn Street, Boston, MA 02215 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **HERBORDT, Martin, C.** [US/US]; 29 Aqueduct Road, Wayland, MA 01778 (US). **VAN COURT, Thomas** [US/US]; 3 Marshall Place, Charlestown, MA 02129 (US).

(74) Agents: **GAGNEBIN, Charles, L., III** et al.; Weingarten, Schurgin, Gagnebin & Lebovici, LLP, Ten Post Office Square, Boston, MA 02109 (US).

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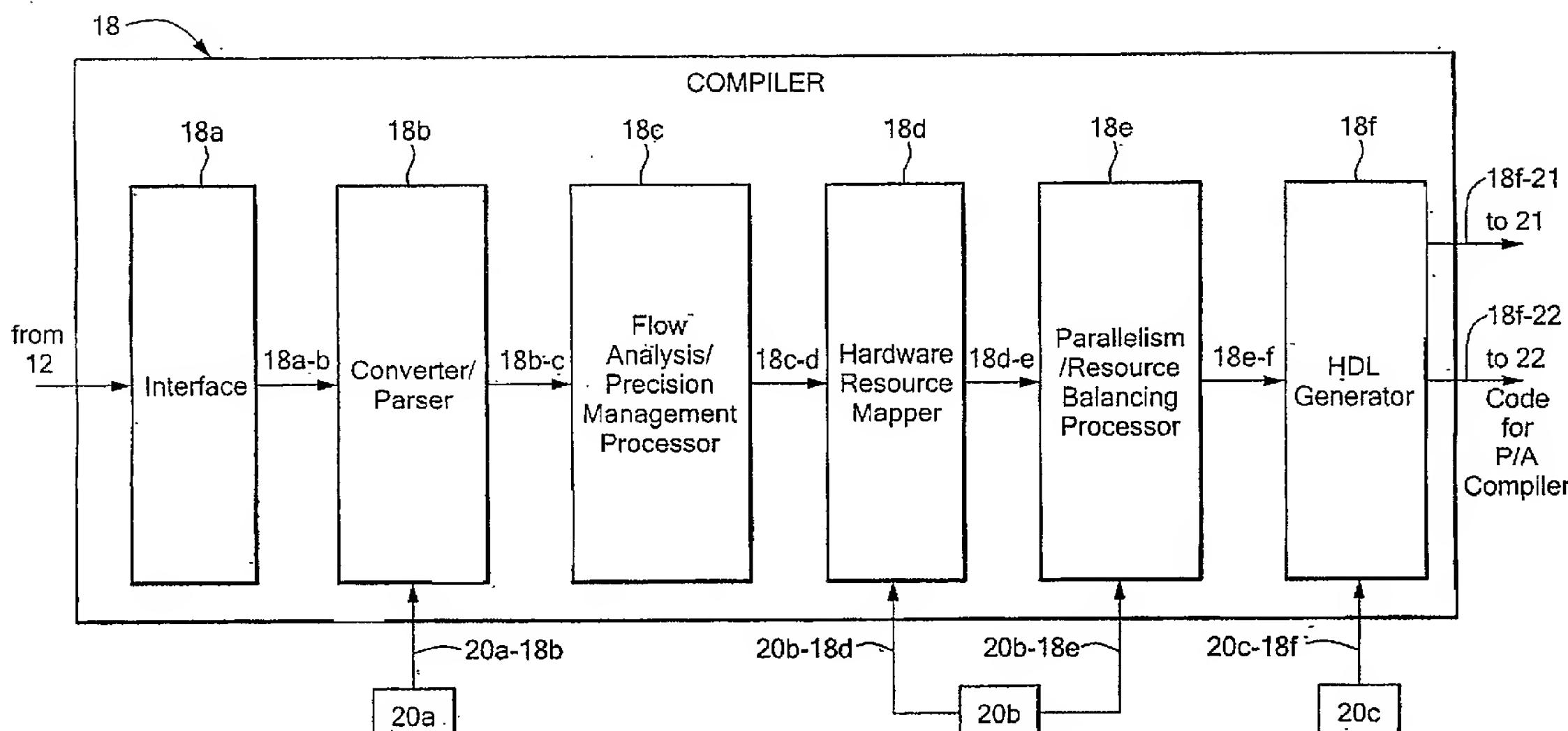
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— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

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(54) Title: SYSTEM AND METHOD FOR PROGRAMMABLE LOGIC ACCELERATOR OF DATA PROCESSING APPLICATIONS AND COMPILER THEREFORE



(57) Abstract: An accelerated processor for use in massive data manipulations specific to an application. A workstation has a general purpose processor and a coprocessor connection to which an application specific coprocessor system is connected. The coprocessor system has programming code which is assembled as instructions for the specific application in combination with accelerator environment specific requirements, independently provided but designed to allow non design experts to input application specific code.

WO 2005/086746 A2



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- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations*
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